Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
14	438	(field adj programmable or FPD) and clock\$1 and global adj1 clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L5	593	(field adj programmable or FPD or FPGA or PLD) and clock\$1 and global adj1 clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L6	38	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and global adj1 clock and (sequential near4 element\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L7	26	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and global adj1 clock\$1 and (sequential adj1 element\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L8	1	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and global adj1 clock\$1 and (base adj1 sequential adj1 element\$1 or modified adj1 sequential adj1 element\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2005/11/22 18:23
L9	125	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and global adj1 clock and transition\$3 and skew	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L10	192	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and global adj1 clock\$1 and skew and (mux or multiplex\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L11	255	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and first near3 clock\$1 and global adj1 clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23

L12	238	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and second adj1 clock\$1 and skew and (mux or multiplex\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L13	400	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and global adj1 clock\$1 and (sequential adj1 element\$1 or flip-flop\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:26
L14	601	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and global adj clock\$1 and (sequential adj1 element\$1 or flip-flop\$1 or register\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L15	201	L14 not L13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:23
L16	394	(field adj programmable or FPD or FPGA or PLD or field near4 array\$1 or programmable near3 device\$1 or field near4 device\$1) and global adj1 clock\$1 and (base adj1 sequential adj1 element\$1 or flip-flop\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:28
L17	382	(field adj programmable or FPD or FPGA or PLD or programmable near3 device\$1) and global adj1 clock\$1 and (base adj1 sequential adj1 element\$1 or flip-flop\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:28
L18	152	(field adj programmable or FPD or FPGA or PLD or programmable near3 device\$1) and global adj1 clock\$1 and (base adj1 sequential adj1 element\$1 or flip-flop\$1) and transition\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/22 18:29